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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/998,131	12/03/2001	Ji Soo Park	0465-0881P	5713

2292 7590 05/01/2003

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EXAMINER
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KESHAVAN, BELUR V

ART UNIT	PAPER NUMBER
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2825

DATE MAILED: 05/01/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

**Advisory Action**

Application No.

09/998,131

Applicant(s)

PARK ET AL.

Examiner

Belur V Keshavan

Art Unit

2825

--The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

THE REPLY FILED 16 April 2003 FAILS TO PLACE THIS APPLICATION IN CONDITION FOR ALLOWANCE. Therefore, further action by the applicant is required to avoid abandonment of this application. A proper reply to a final rejection under 37 CFR 1.113 may only be either: (1) a timely filed amendment which places the application in condition for allowance; (2) a timely filed Notice of Appeal (with appeal fee); or (3) a timely filed Request for Continued Examination (RCE) in compliance with 37 CFR 1.114.

**PERIOD FOR REPLY** [check either a) or b)]

- a) ☒ The period for reply expires 2 months from the mailing date of the final rejection.  
b) ☐ The period for reply expires on: (1) the mailing date of this Advisory Action, or (2) the date set forth in the final rejection, whichever is later. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of the final rejection.  
ONLY CHECK THIS BOX WHEN THE FIRST REPLY WAS FILED WITHIN TWO MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f).

Extensions of time may be obtained under 37 CFR 1.136(a). The date on which the petition under 37 CFR 1.136(a) and the appropriate extension fee have been filed is the date for purposes of determining the period of extension and the corresponding amount of the fee. The appropriate extension fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened statutory period for reply originally set in the final Office action; or (2) as set forth in (b) above, if checked. Any reply received by the Office later than three months after the mailing date of the final rejection, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

1. ☐ A Notice of Appeal was filed on \_\_\_\_\_. Appellant's Brief must be filed within the period set forth in 37 CFR 1.192(a), or any extension thereof (37 CFR 1.191(d)), to avoid dismissal of the appeal.  
2. ☐ The proposed amendment(s) will not be entered because:  
(a) ☐ they raise new issues that would require further consideration and/or search (see NOTE below);  
(b) ☐ they raise the issue of new matter (see Note below);  
(c) ☐ they are not deemed to place the application in better form for appeal by materially reducing or simplifying the issues for appeal; and/or  
(d) ☐ they present additional claims without canceling a corresponding number of finally rejected claims.

NOTE: \_\_\_\_\_

3. ☐ Applicant's reply has overcome the following rejection(s): \_\_\_\_\_.  
4. ☐ Newly proposed or amended claim(s) \_\_\_\_\_ would be allowable if submitted in a separate, timely filed amendment canceling the non-allowable claim(s).  
5. ☒ The a) ☐ affidavit, b) ☐ exhibit, or c) ☒ request for reconsideration has been considered but does NOT place the application in condition for allowance because: Please see the attached Remarks.  
6. ☐ The affidavit or exhibit will NOT be considered because it is not directed SOLELY to issues which were newly raised by the Examiner in the final rejection.  
7. ☐ For purposes of Appeal, the proposed amendment(s) a) ☐ will not be entered or b) ☐ will be entered and an explanation of how the new or amended claims would be rejected is provided below or appended.

The status of the claim(s) is (or will be) as follows:

Claim(s) allowed: \_\_\_\_\_

Claim(s) objected to: \_\_\_\_\_

Claim(s) rejected: 1-21.

Claim(s) withdrawn from consideration: \_\_\_\_\_

8. ☐ The proposed drawing correction filed on \_\_\_\_\_ is a) ☐ approved or b) ☐ disapproved by the Examiner.  
9. ☐ Note the attached Information Disclosure Statement(s) (PTO-1449) Paper No(s).  
10. ☐ Other: \_\_\_\_\_

*W. J. S. O.*  
RECEIVED  
SUPERVISOR'S OFFICE  
FROM 2/26/03 2:00

**Remarks**

Applicants' Remarks of 04/16/2003 in response to Final Office Action of 12/16/2003 requesting for reconsideration of the application have been fully considered.

The examiner has noted applicants' remarks that Doyle neither states nor implies that it is not a requirement that the gate region be silicided. Doyle et al. teach in column 3 and lines 55-64 that in their invention it is not requirement that source/drain regions be silicided which means that the first silicide layer (110) is not needed. Therefore the sacrificial dielectric layer (114) will make a contact with the surface of the gate. Doyle et al. disclose forming a dielectric (114) on the exposed top and side surfaces and on the entire surface of the substrate and planarizing the substrate by CMP to expose the top surface of the gate electrode through the dielectric layer. Therefore it is obvious that the dielectric layer is thicker than the gate. Doyle et al. disclose forming subsequently gate silicide on the exposed gate electrode, which reduces the gate resistance.

Applicants' Remarks of 04/16/2003 in response to Final Office Action of 12/16/2003 requesting for reconsideration of the application have been fully considered and are not persuasive for the reasons given above.

Further, layer 110 is formed "concurrently" with layers 112. If layer 112 is not formed as suggested by Doyle, then 110 is also not formed. The embodiment which includes 110 is just that "an embodiment" and not the only embodiment suggested. mms

BK